

Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=100V, V_{GS}=0V$	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$	-	-	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.5	2.2	V
$R_{DS(on)}$	Static Drain-Source on-Resistance <small>note2</small>	$V_{GS}=10V, I_D=2A$	-	220	250	m Ω
		$V_{GS}=4.5V, I_D=1A$	-	255	280	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=25V, V_{GS}=0V,$ $f=1.0MHz$	-	321	-	pF
C_{oss}	Output Capacitance		-	21	-	pF
C_{rss}	Reverse Transfer Capacitance		-	15	-	pF
Q_g	Total Gate Charge	$V_{DS}=30V, I_D=2A,$ $V_{GS}=10V$	-	5.3	-	nC
Q_{gs}	Gate-Source Charge		-	1.3	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	1.7	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=30V,$ $I_D=1A, R_{GEN}=3\Omega,$ $V_{GS}=10V$	-	14	-	ns
t_r	Turn-on Rise Time		-	54	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	18	-	ns
t_f	Turn-off Fall Time		-	11	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	3.0	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	8.8	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_S=2.2A$	-	-	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Typical Performance Characteristics

Figure 1: Output Characteristics

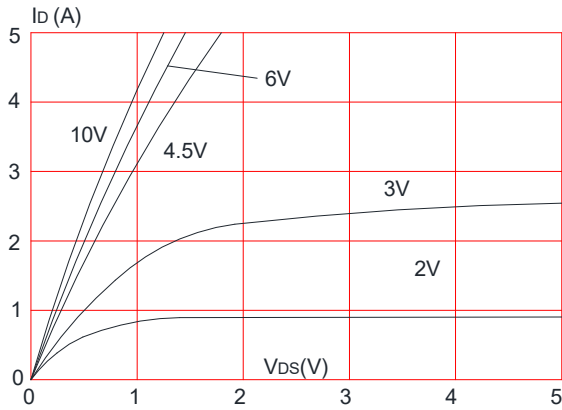


Figure 2: Typical Transfer Characteristics

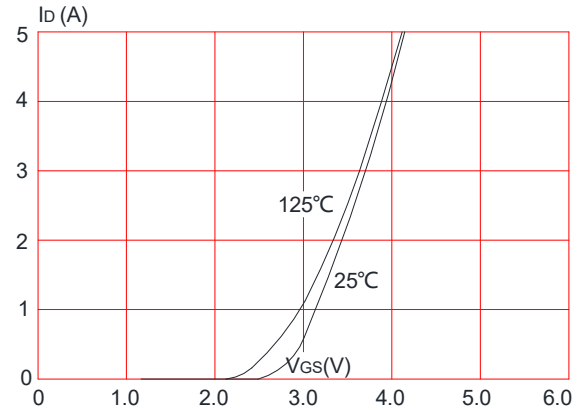


Figure 3: On-resistance vs. Drain Current

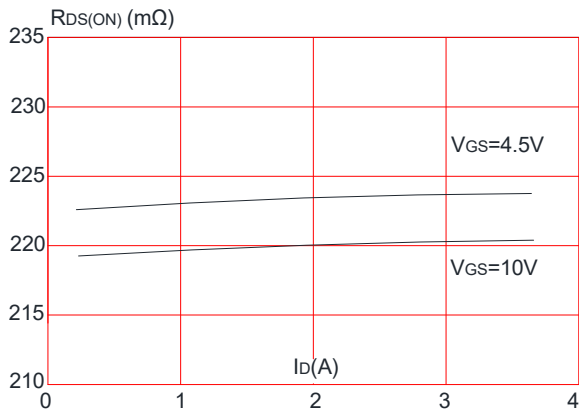


Figure 4: Body Diode Characteristics

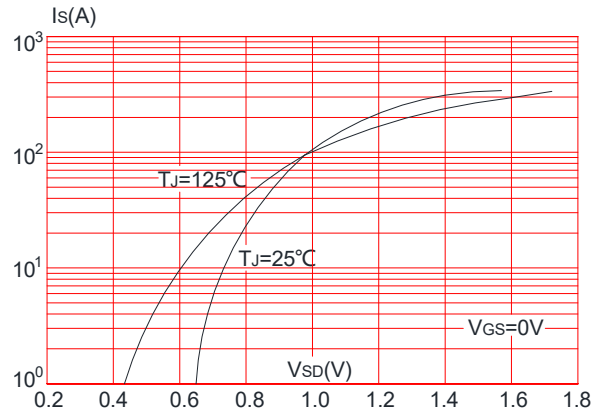


Figure 5: Gate Charge Characteristics

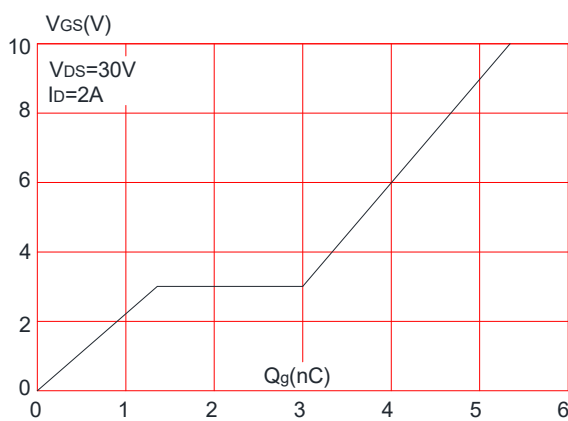


Figure 6: Capacitance Characteristics

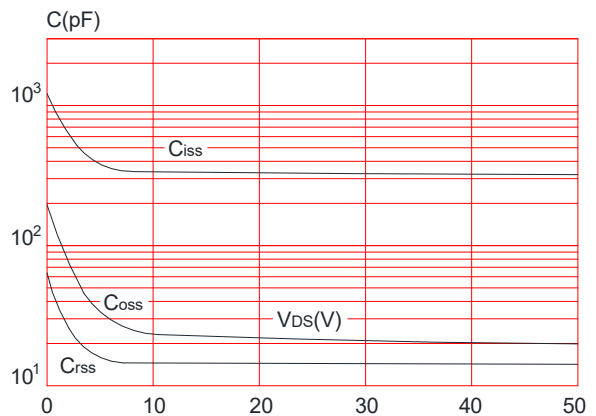


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

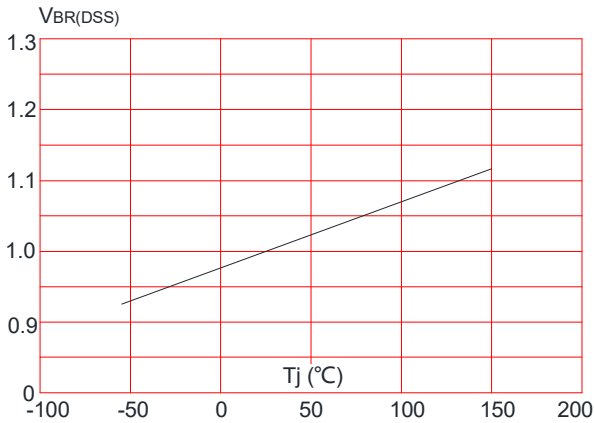


Figure 8: Normalized on Resistance vs. Junction Temperature

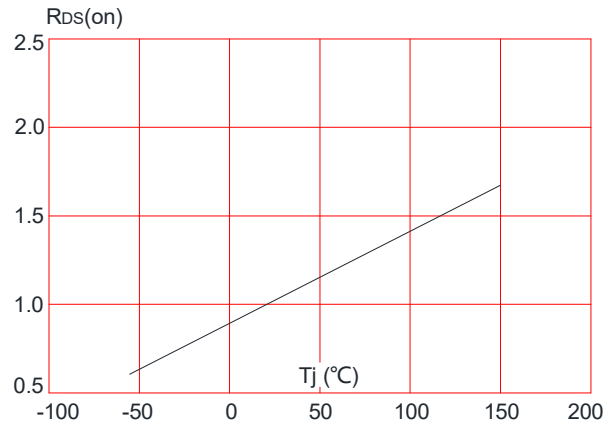


Figure 9: Maximum Safe Operating Area

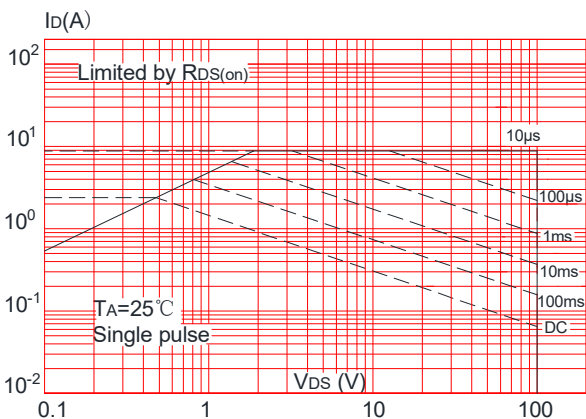


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

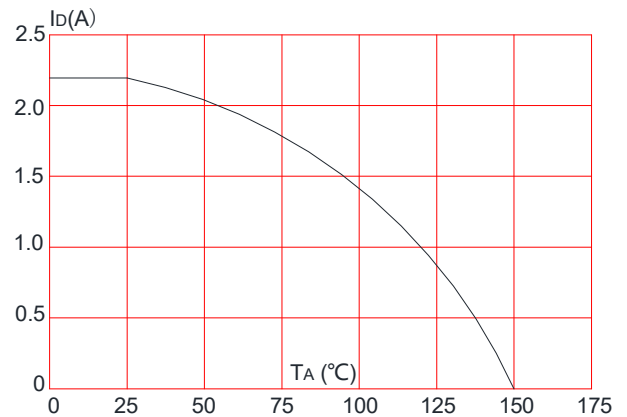
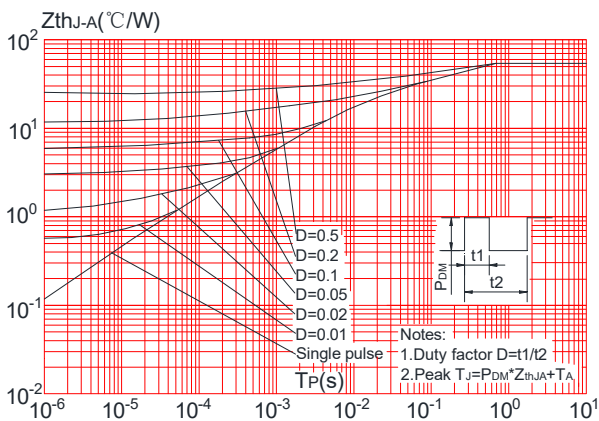


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



Test Circuit

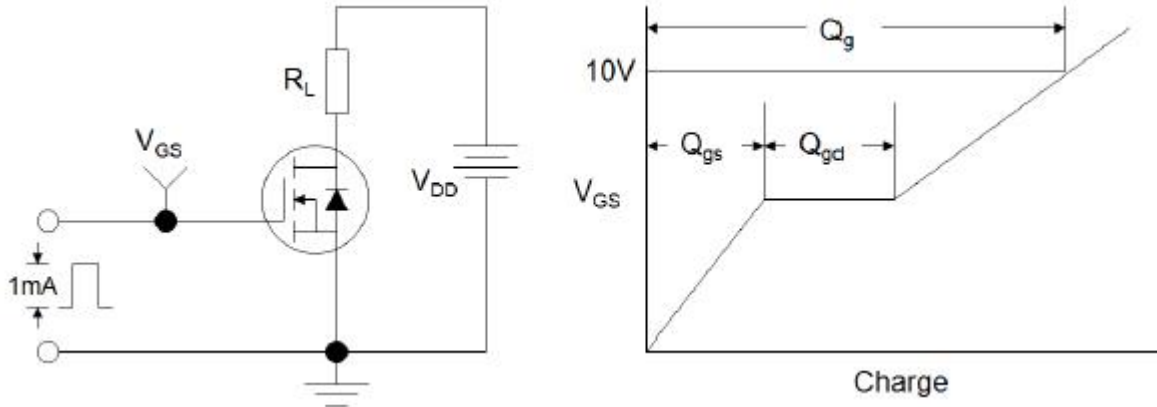


Figure1:Gate Charge Test Circuit & Waveform

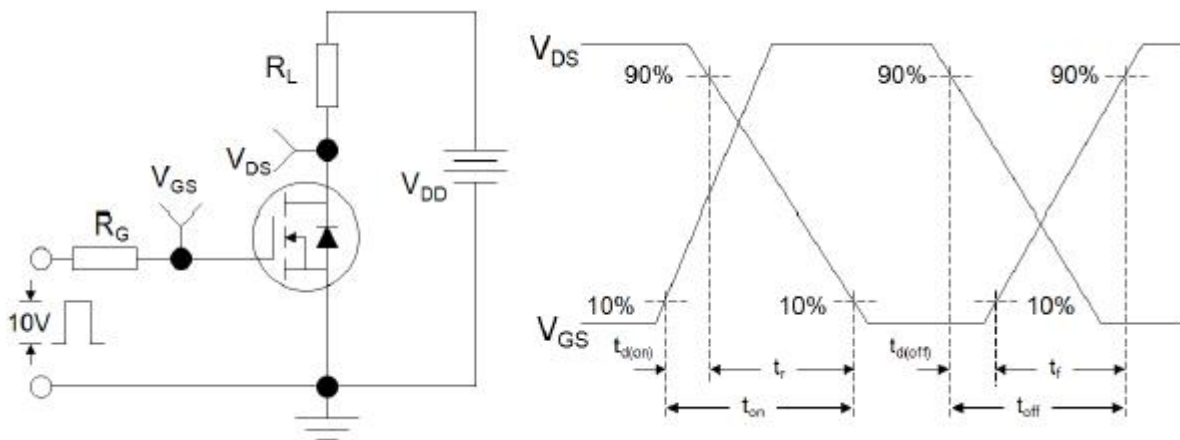


Figure 2: Resistive Switching Test Circuit & Waveforms

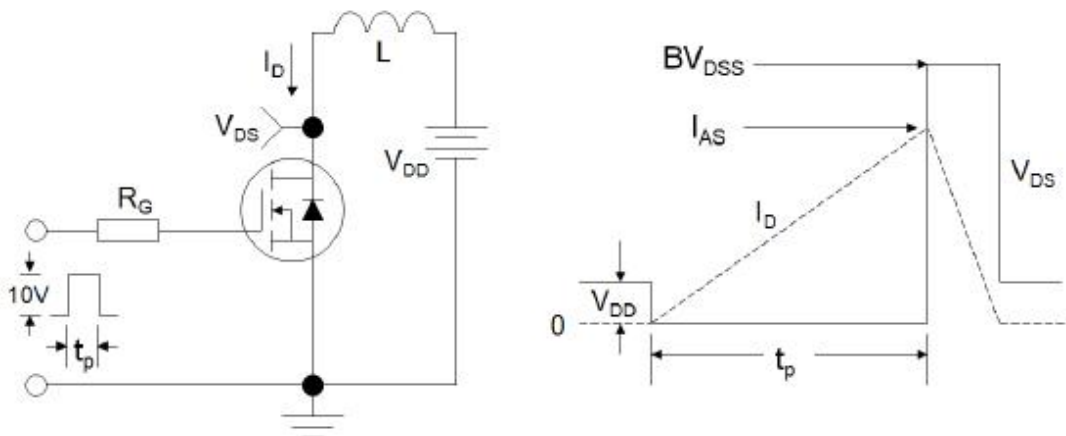


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

Package Mechanical Data SOT23

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