



TM120N03NF

N-Channel Enhancement Mosfet

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

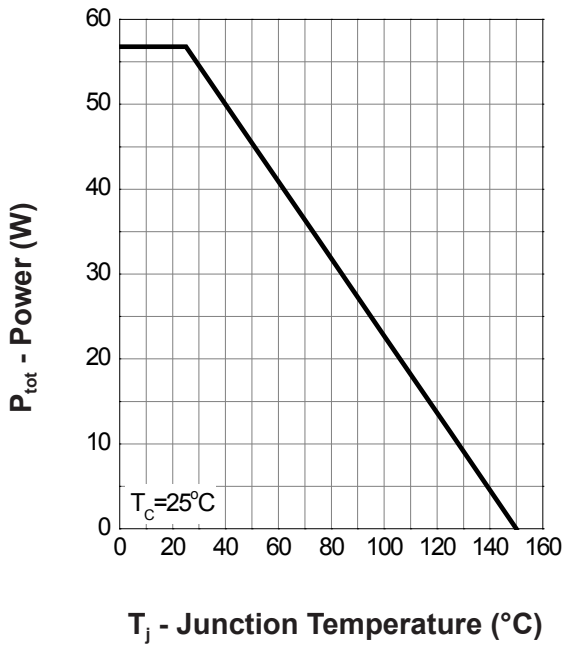
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=250\mu A$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=24V, V_{GS}=0V$ $T_J=85^\circ\text{C}$	-	-	1	μA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu A$	1.4	1.7	2.5	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
$R_{DS(on)}^d$	Drain-Source On-state Resistance	$V_{GS}=10V, I_{DS}=20A$ $T_J=125^\circ\text{C}$	-	3	3.8	m Ω
		$V_{GS}=4.5V, I_{DS}=15A$	-	4.4	-	
Gfs	Forward Transconductance	$V_{DS}=5V, I_{DS}=10A$	-	24.6	-	S
Diode Characteristics						
V_{SD}^d	Diode Forward Voltage	$I_{SD}=20A, V_{GS}=0V$	-	0.8	1.1	V
t_{rr}	Reverse Recovery Time	$I_{DS}=20A, di_{SD}/dt=100A/\mu s$	-	35.6	-	ns
t_a	Charge Time		-	19.3	-	
t_b	Discharge Time		-	16.3	-	
Q_{rr}	Reverse Recovery Charge		-	26	-	
Dynamic Characteristics ^e						
R_G	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1\text{MHz}$	-	1	2	Ω
C_{iss}	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=15V,$ Frequency=1.0MHz	-	2485	2971	pF
C_{oss}	Output Capacitance		-	850	-	
C_{rss}	Reverse Transfer Capacitance		-	85	-	
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=15V, R_L=15\Omega,$ $I_{DS}=1A, V_{GEN}=10V,$ $R_G=6\Omega$	-	12.4	23	ns
t_r	Turn-on Rise Time		-	9.5	18	
$t_{d(OFF)}$	Turn-off Delay Time		-	27.2	49	
t_f	Turn-off Fall Time		-	35.2	64	
Gate Charge Characteristics ^e						
Q_g	Total Gate Charge	$V_{DS}=15V, V_{GS}=10V,$ $I_{DS}=20A$	-	20.6	28.8	nC
Q_g	Total Gate Charge	$V_{DS}=15V, V_{GS}=4.5V,$ $I_{DS}=20A$	-	9.8	-	
Q_{gth}	Threshold Gate Charge		-	1.8	-	
Q_{gs}	Gate-Source Charge		-	3.8	-	
Q_{gd}	Gate-Drain Charge		-	3.7	-	

Note d : Pulse test ; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

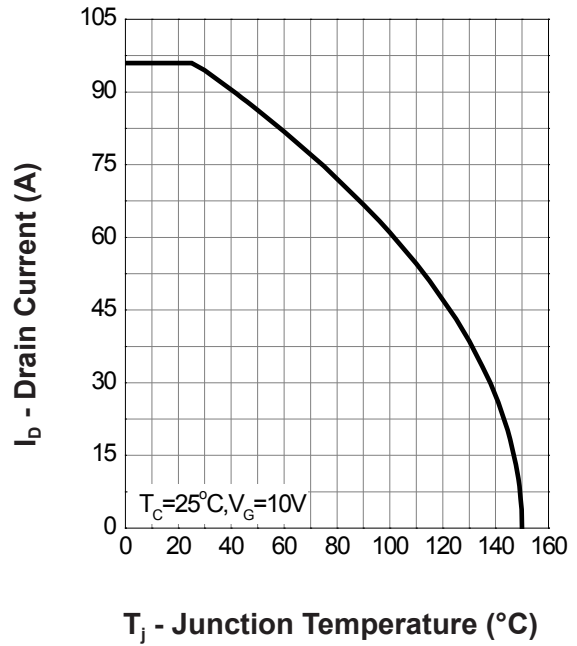
Note e : Guaranteed by design, not subject to production testing.

Typical Operating Characteristics

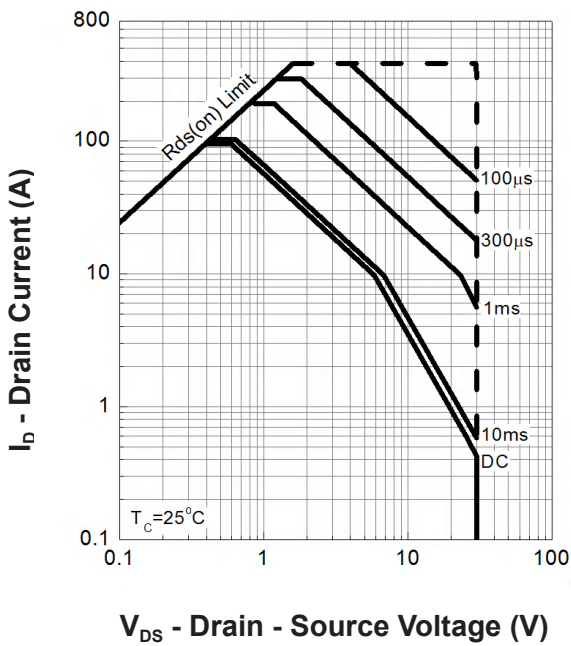
Power Dissipation



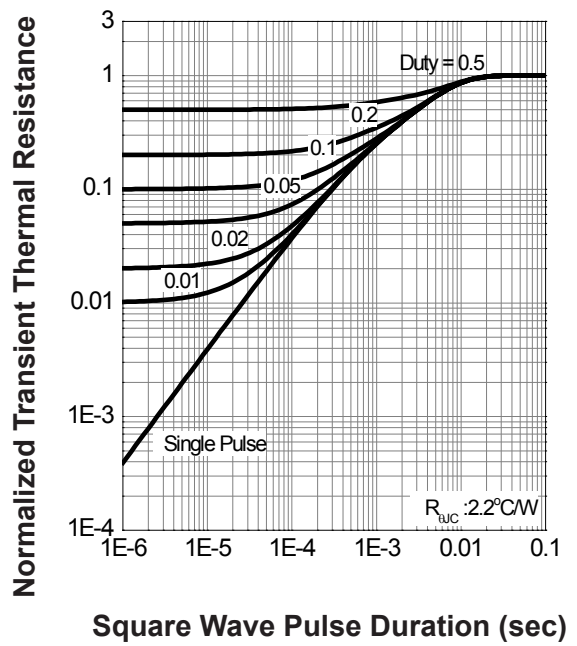
Drain Current



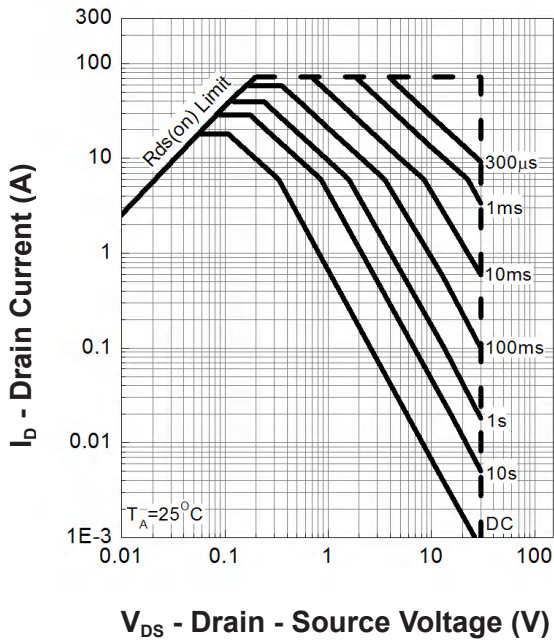
Safe Operation Area



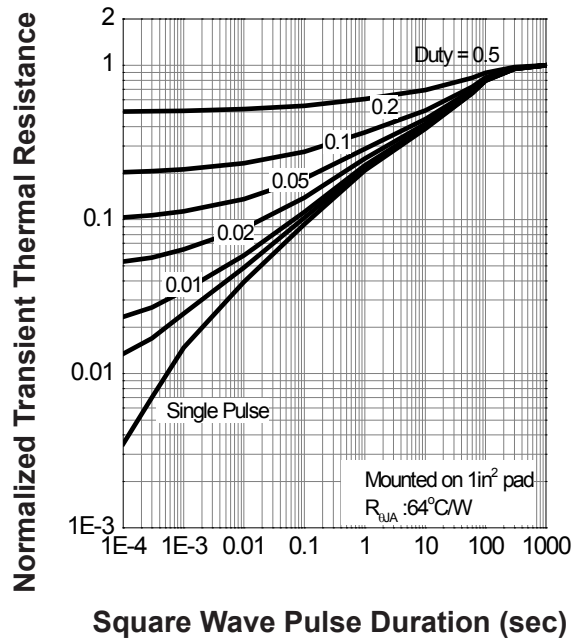
Thermal Transient Impedance



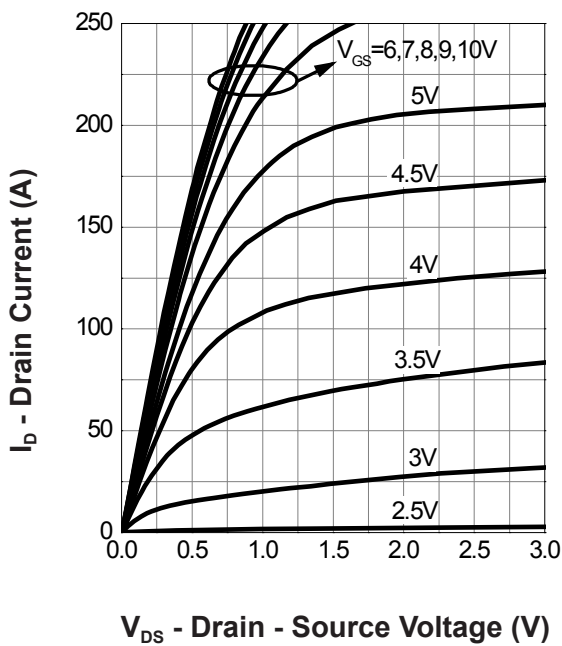
Safe Operation Area



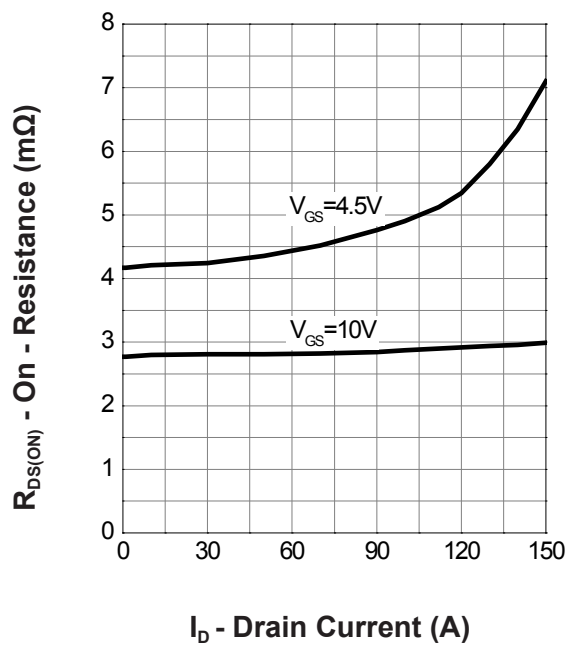
Thermal Transient Impedance



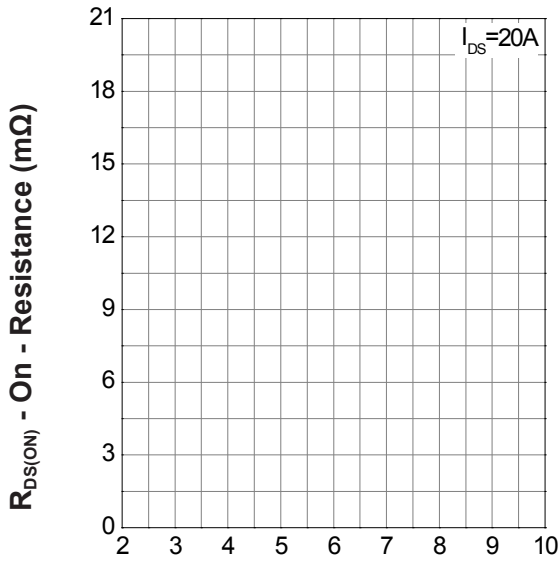
Output Characteristics



Drain-Source On Resistance

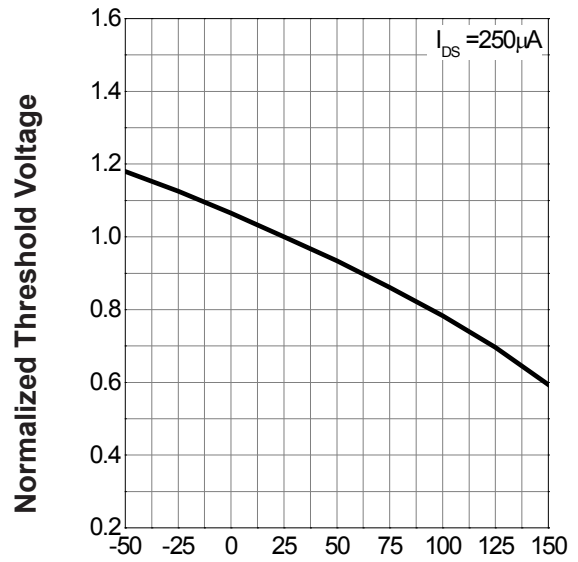


Gate-Source On Resistance



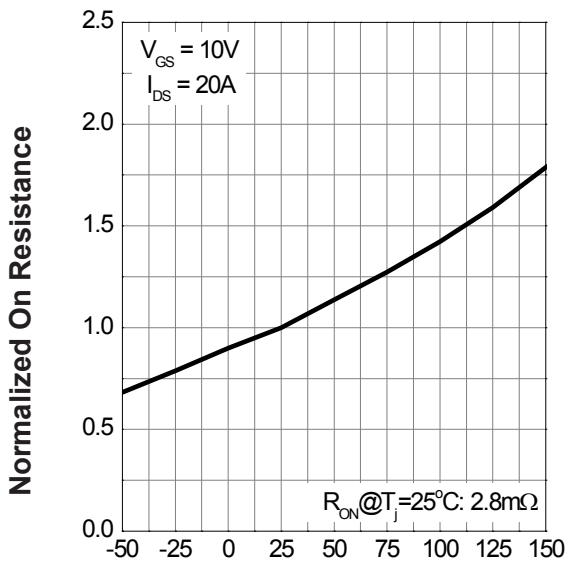
V_{GS} - Gate - Source Voltage (V)

Gate Threshold Voltage



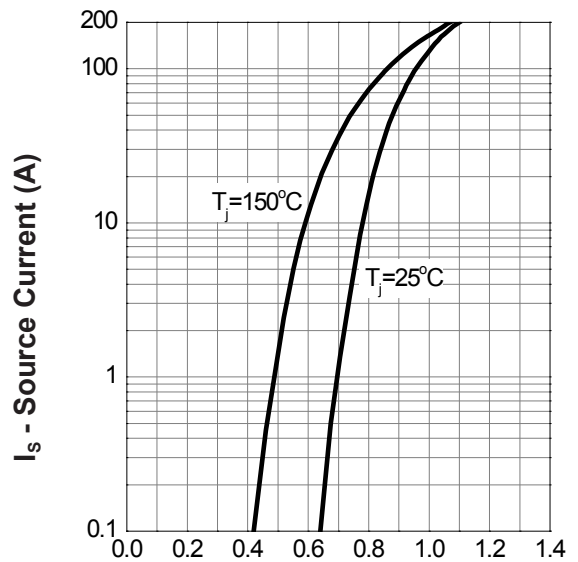
T_J - Junction Temperature (°C)

Drain-Source On Resistance



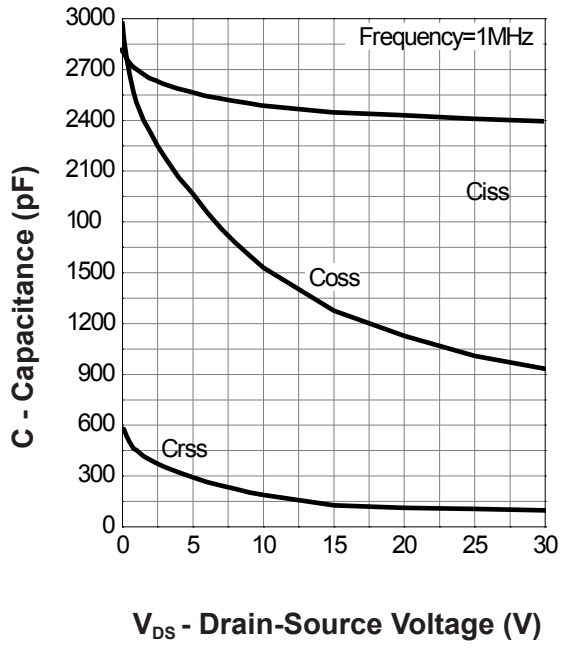
T_J - Junction Temperature (°C)

Source-Drain Diode Forward

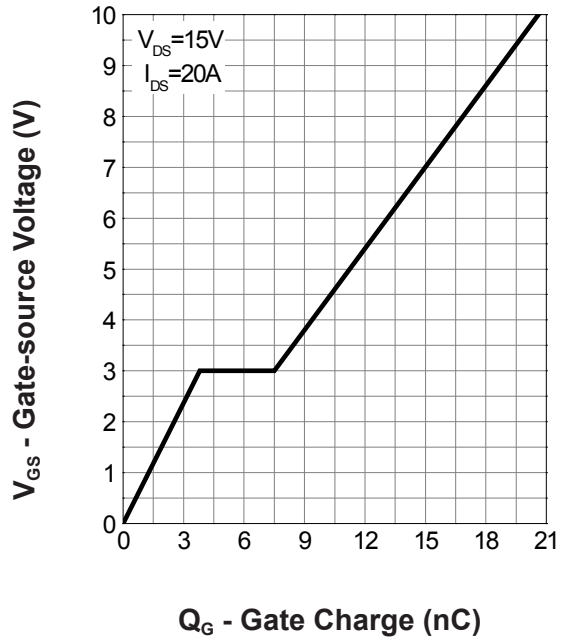


V_{SD} - Source - Drain Voltage (V)

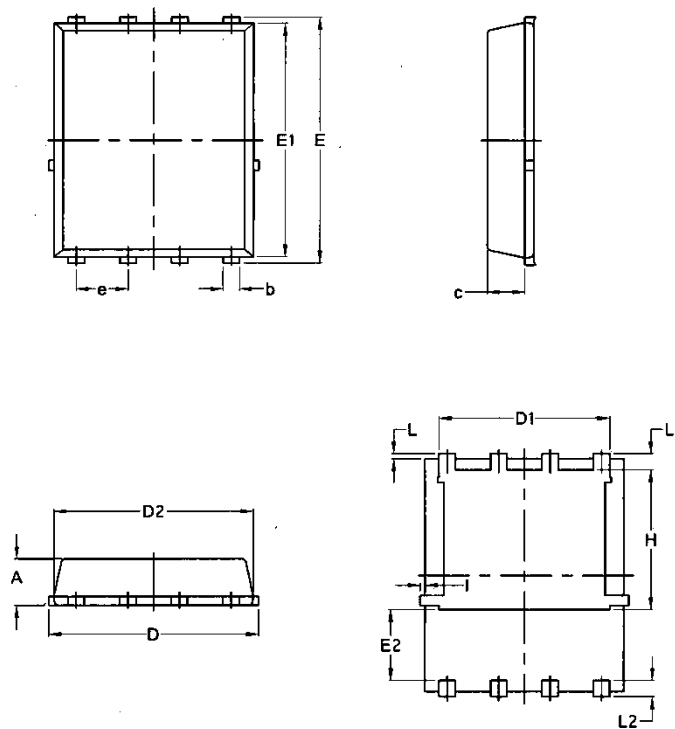
Capacitance



Gate Charge



Package Mechanical Data:DFN5x6-8L



Symbol	Common			
	mm		Inch	
	Min	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.0970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	/	0.18	/	0.0070