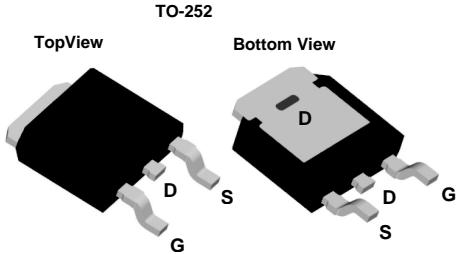
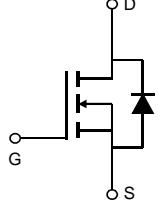


# TMD80N04D

## N-CHANNEL ENHANCEMENT MOSFET

<p><b>General Description</b></p> <p>The TMD80N04D uses advanced trench technology and design to provide excellent <math>R_{DS(ON)}</math> with low gate charge. It can be used in a wide variety of applications.</p>	<p><b>Product Summary</b></p> <p>VDS = 40V, ID = 80A RDS(ON) &lt; 7m @ VGS=10V  100% UIS Tested 100% <math>R_g</math> Tested</p>
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### Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	40	V
Gate-Source Voltage	VGS	$\pm 20$	V
Drain Current-Continuous	$I_D$	80	A
Drain Current-Continuous( $T_c=100^\circ\text{C}$ )	$I_D(100^\circ\text{C})$	56	A
Pulsed Drain Current	IDM	350	A
Maximum Power Dissipation	$P_D$	80	W
Derating factor		0.53	W/ $^\circ\text{C}$
Single pulse avalanche energy <sup>(Note 5)</sup>	EAS	750	mJ
Operating Junction and Storage Temperature Range	TJ,TSTG	-55 To 175	$^\circ\text{C}$
Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	$R_{\theta JC}$	1.88	$^\circ\text{C}/\text{W}$

## Electrical Characteristics ( $T_c=25^\circ\text{C}$ unless otherwise noted)

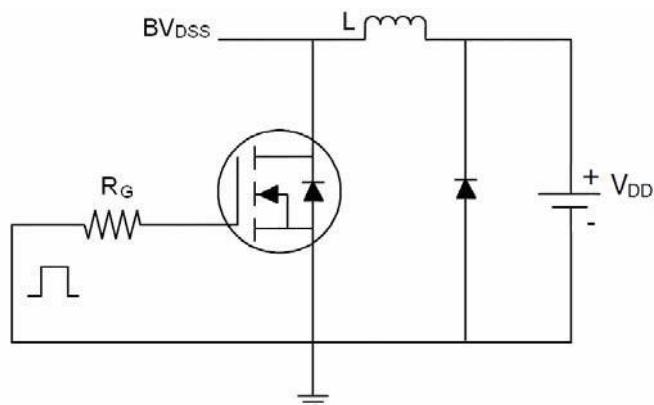
Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	40	45	-	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
Gate-Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b> <sup>(Note 3)</sup>						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.2	1.8	2.5	V
Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=20\text{A}$	-		7	$\text{m}\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=20\text{A}$	15	-	-	S
<b>Dynamic Characteristics</b> <sup>(Note 4)</sup>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	-	2320	PF
Output Capacitance	$C_{\text{oss}}$		-	-	189	PF
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	-	140	PF
<b>Switching Characteristics</b> <sup>(Note 4)</sup>						
Turn-on Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=20\text{V}, R_{\text{L}}=1$ $V_{\text{GS}}=10\text{V}, R_{\text{G}}=3$	-	12	-	nS
Turn-on Rise Time	$t_r$		-	11	-	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	39	-	nS
Turn-Off Fall Time	$t_f$		-	12	-	nS
Total Gate Charge	$Q_g$	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=20\text{A}, V_{\text{GS}}=10\text{V}$	-	61	-	nC
Gate-Source Charge	$Q_{\text{gs}}$		-	15.3	-	nC
Gate-Drain Charge	$Q_{\text{gd}}$		-	14.5	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <sup>(Note 3)</sup>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=10\text{A}$	-		1.2	V
Diode Forward Current <sup>(Note 2)</sup>	$I_s$		-	-	80	A
Reverse Recovery Time	$t_{\text{rr}}$	$T_J = 25^\circ\text{C}, IF = 20\text{A}$ $di/dt = 100\text{A}/\mu\text{s}$ <sup>(Note 3)</sup>	-	-	45	nS
Reverse Recovery Charge	$Q_{\text{rr}}$		-	-	50	nC

### Notes:

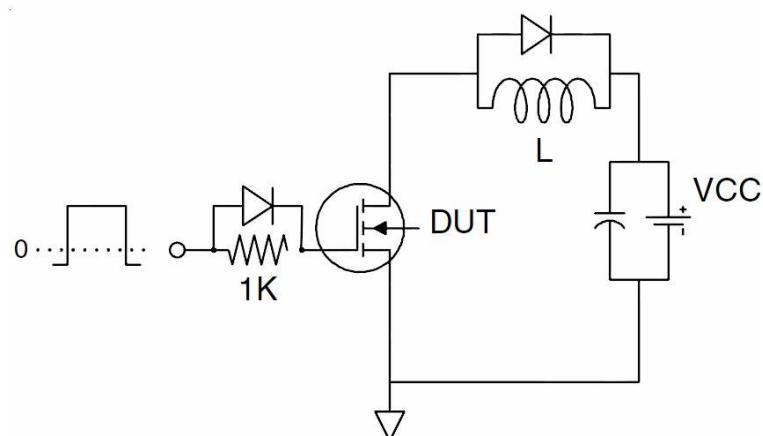
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
  2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
  3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
  4. Guaranteed by design, not subject to production
5.  $E_{\text{AS}}$  condition :  $T_j=25^\circ\text{C}, V_{\text{DD}}=20\text{V}, V_{\text{G}}=10\text{V}, L=1\text{mH}, R_g=25$  ,  $I_{\text{AS}}=42\text{A}$

## Test circuit

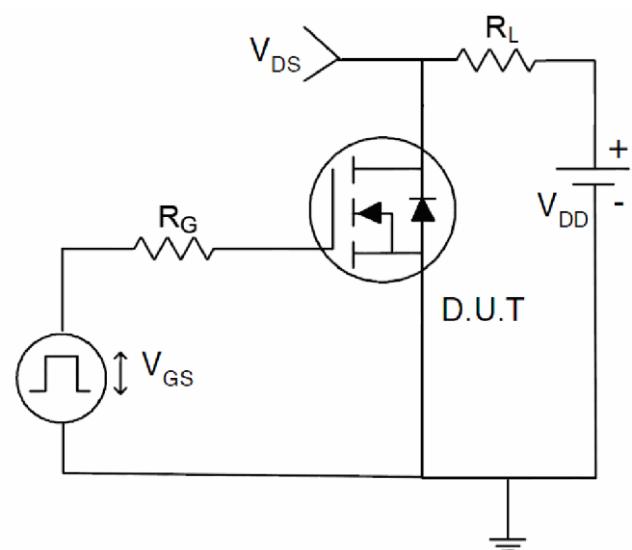
### 1) E<sub>AS</sub> Test Circuit



### 2) Gate Charge Test Circuit



### 3) Switch Time Test Circuit



## Typical Electrical and Thermal Characteristics (Curves)

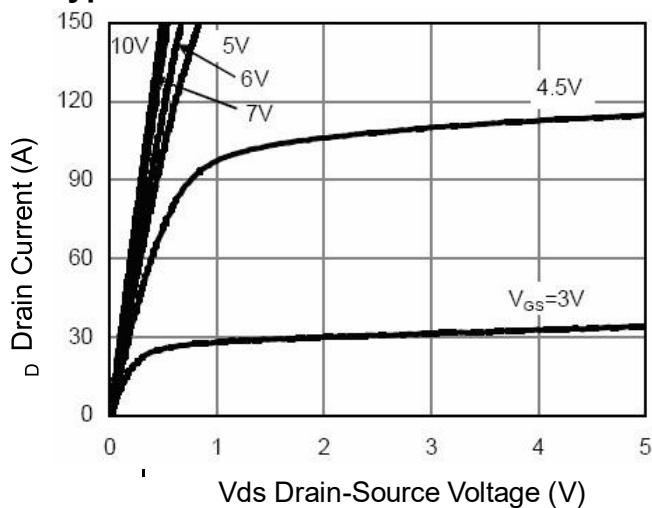


Figure 1 Output Characteristics

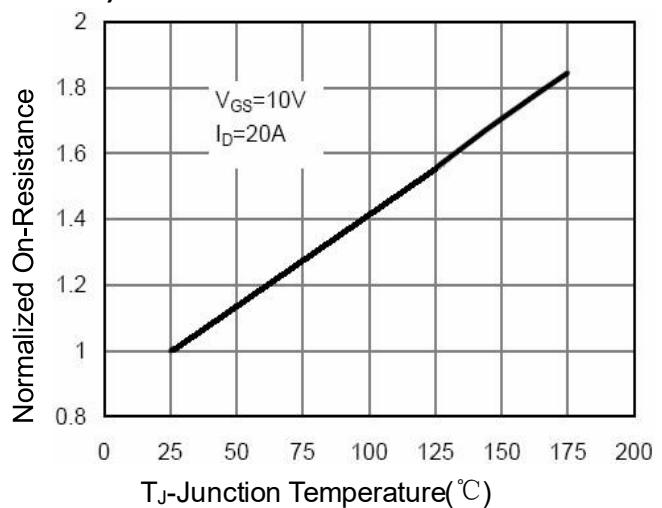


Figure 4 Rdson-JunctionTemperature

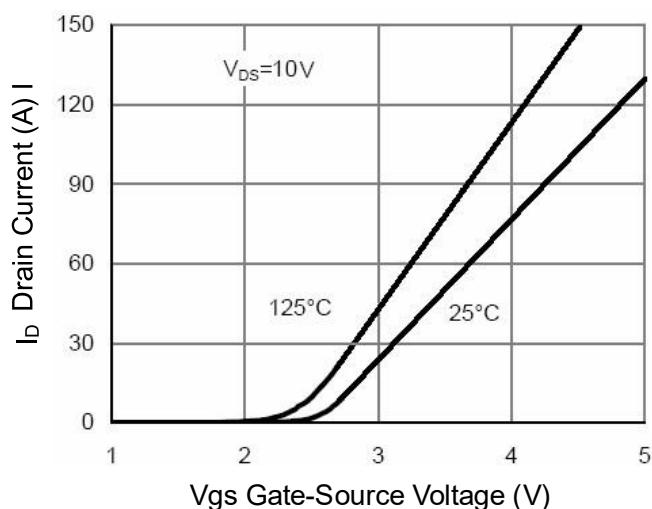


Figure 2 Transfer Characteristics

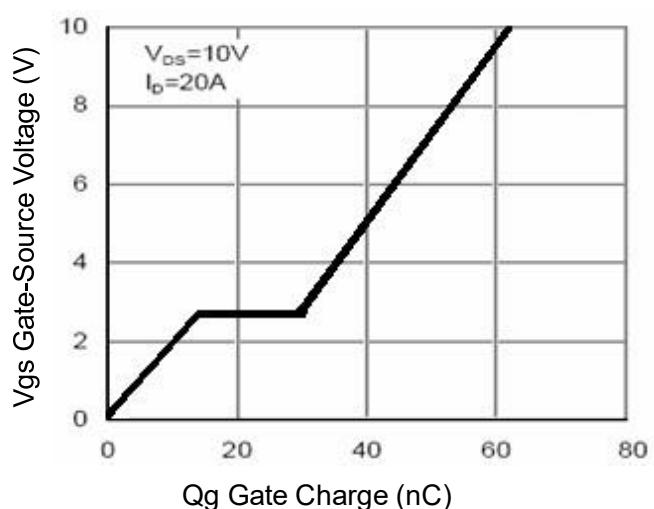


Figure 5 Gate Charge

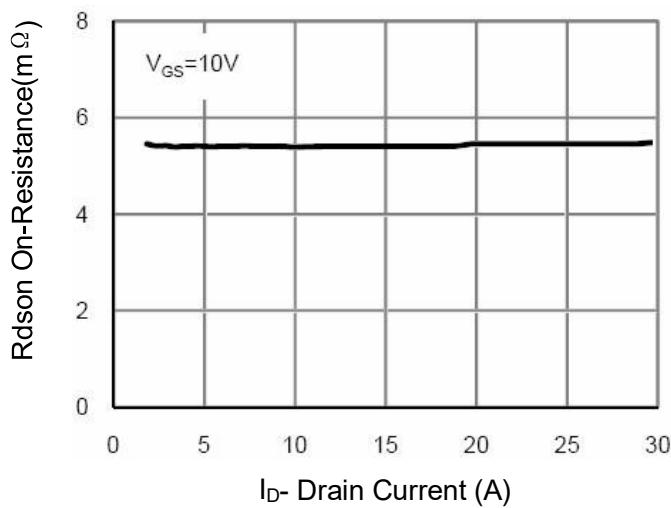


Figure 3 Rdson-Drain Current

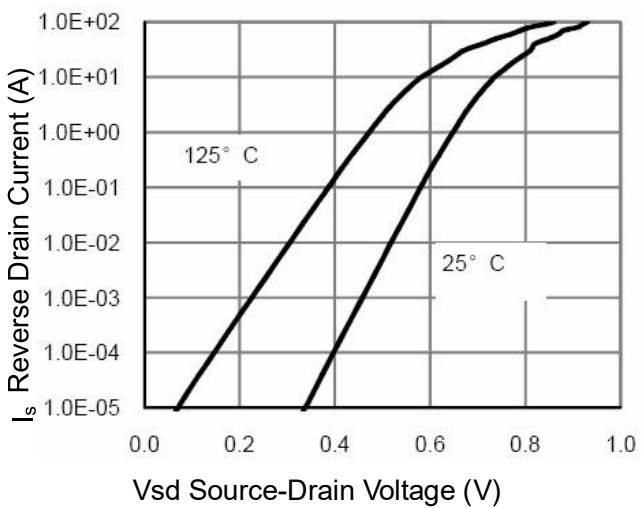
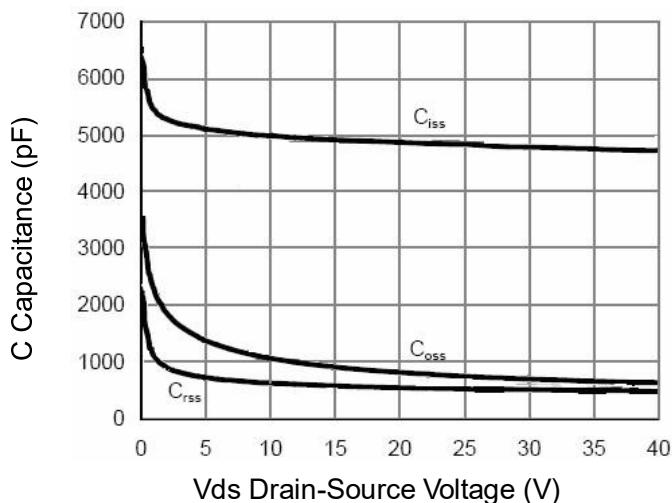
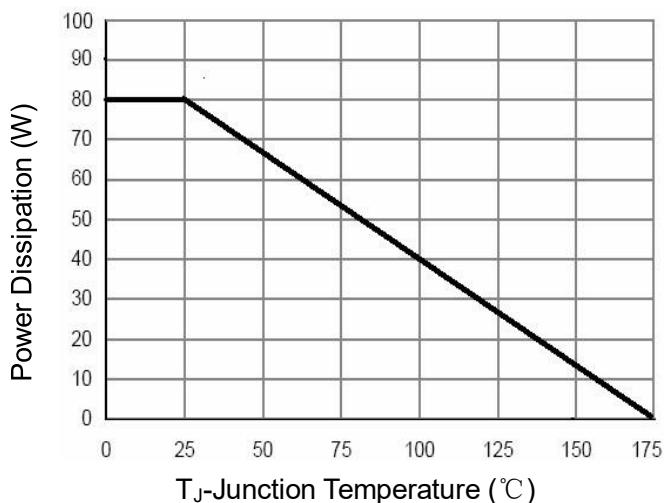


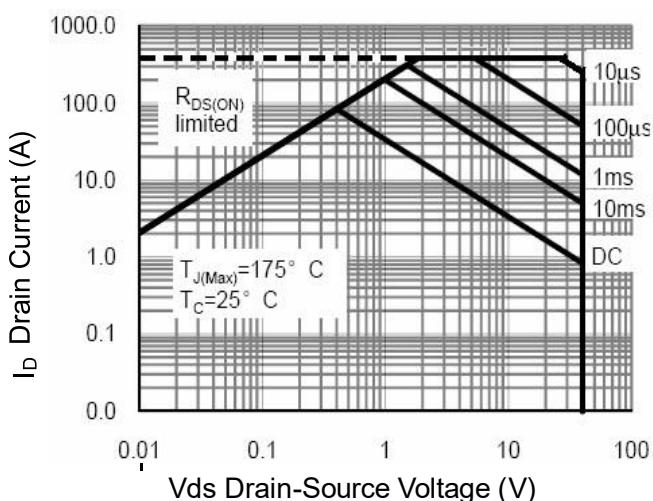
Figure 6 Source-Drain Diode Forward



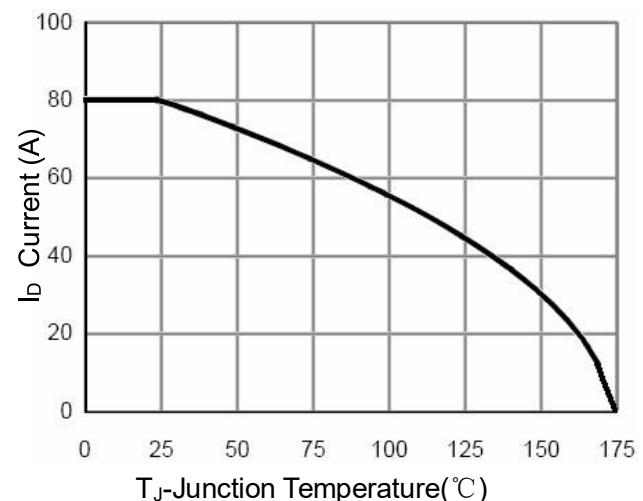
**Figure 7 Capacitance vs Vds**



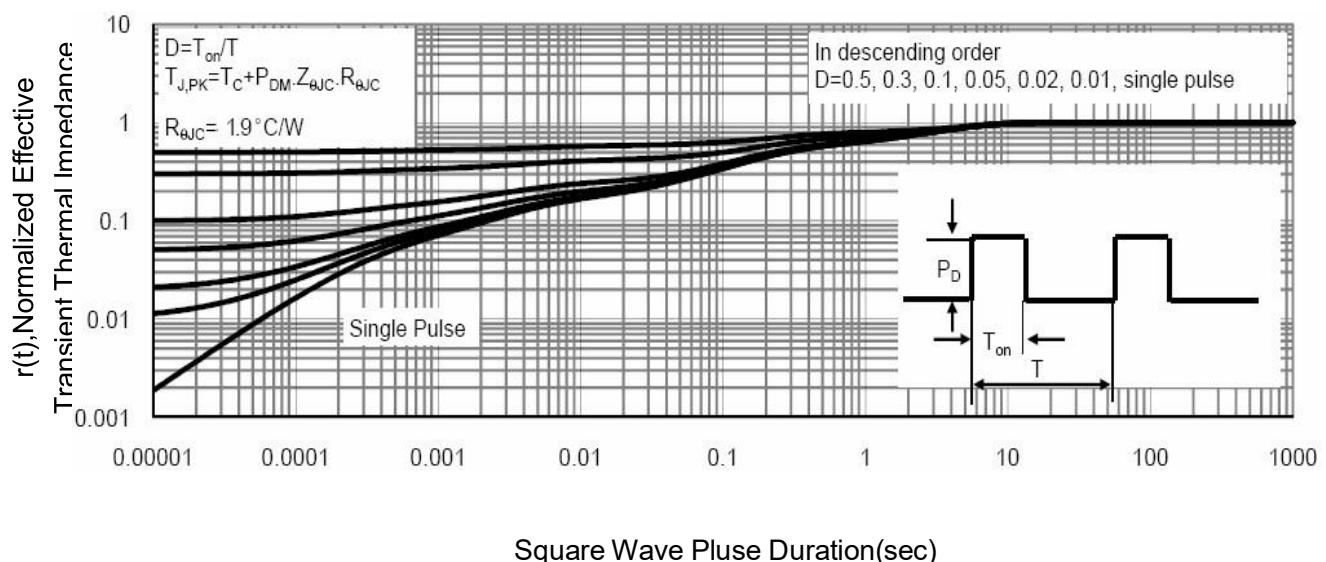
**Figure 9 Power De-rating**



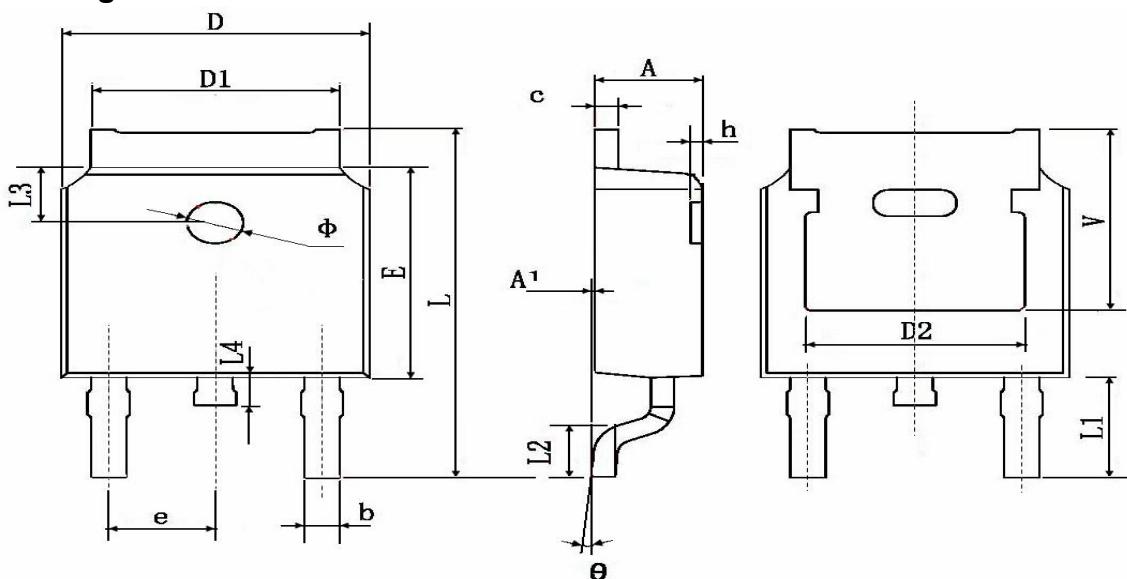
**Figure 8 Safe Operation Area**



**Figure 10 ID Current- Junction Temperature**



## TO-252 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	